

SEP 19 2005



LSI Logic Corporation
Intellectual Property
Legal Department, MS D-106
1621 Barber Lane
Milpitas, CA 95035

Date September 19, 2005

Number of pages including cover sheet 2

To: Examiner Dmitri Levitan

US PTO

Fax No. 571-273-8300

Phone No.

CC:

From: Connie del Castillo

Intellectual Property Paralegal

Telephone No. (408) 433-7191

Fax No. (408) 433-7460

REMARKS:

☐ Urgent

☐ For your review

☒ Reply ASAP

☐ Please comment

Application Number: 09/820,477
Filing date: December 3, 2003
First named inventor: Jessie Sandoval
Attorney docket number: 99-413

Transmitted herewith via facsimile:

- Response to examiner's claim amendment proposal

Pursuant to 37 C.F.R. 1.8, I hereby certify that this correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date indicated below:

Date

9/19/05

Signature

Connie del Castillo

Connie del Castillo

Please contact us at (408) 433-7191 if you do not receive all pages indicated above or experience any difficulty in receiving this facsimile.

This Facsimile is intended only for the use of the addressee and, if the addressee is a client or their agent, contains privileged and confidential information. If you are not the recipient of this facsimile, you have received this facsimile inadvertently and in error. Any review, dissemination, distribution or other use is strictly prohibited. If you received this facsimile in error, please notify us by telephone and return the facsimile to us.

LSI Logic
Corporation

LSI LOGIC®

1621 Barber Lane
Milpitas CA 95035

408.433.8000

Date: 09/19/2005

To: Examiner Dmitri Levitan
From: Henry GrothRegarding:
Patent Application No. 09/820,477
Title: Data Packet Congestion Management Technique
Inventor: Jessie Sandoval
Docket No. 99-413Response to examiner's claim amendment proposal

Dear Examiner Levitan:

Per our discussion earlier today, please consider the following proposed amendment to claim 1 in the above-referenced application.

Claim 1:

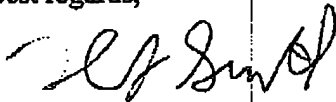
A circuit comprising:

a buffer for storing a plurality of data packets; and
a test circuit configured to: (i) monitor a number of said plurality of data packets in said buffer, (ii) receive an additional data packet to said plurality of data packets, (iii) store said additional data packet into said buffer responsive to said number being less than a first threshold, (iv) discard said additional data packet in accordance with a probabilistic test responsive to said number being greater than said first threshold, and (v) presenting an identification signal to a sender of said additional data packet, indicating that said additional data packet as was discarded and can be resent.

Support for this amendment in the detailed description can be found at page 13, lines 4-6 of the originally submitted patent application.

Please contact me at your earliest convenience if further discussion of this proposed amendment is needed.

Best regards,



Henry Groth, Corporate Counsel
Intellectual Property, Corporate Legal Department, LSI Logic Corporation
1621 Barber Lane, Milpitas, CA 95035
Phone: (408) 433-4578
e-mail: hgroth@lsil.com